



FORM PTO-149 U.S. DEPARTMENT OF COMMERCE
MODIFIED TRADEMARK AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet	1	of	2	Application Number	10/822,908
				Filing Date	April 12, 2004
				First Named Inventor	Rina Panigrahy
				Group Art Unit	2661
				Examiner Name	Phunkulh, Bob A
				Attorney Docket No.	00240

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	Cite No.	Document Number	ISSUE or PUBL. DATE (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document
BAP	AA	US - 4,733,390	02-22-1988	Shirakawa et al.
↓	AB	US - 5,500,858	03-19-1996	McKeown
↓	AC	US - 5,923,644	07-13-1999	McKeown et al.
✓	AD	US - 6,212,182	04-03-2001	McKeown

U.S. PUBLISHED APPLICATIONS

EXAMINER'S INITIALS	Cite No.	Document Number	ISSUE or PUBL. DATE (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document
BAP	AE	US 2003/0193941	10-16-2003	Bonomi et al.
BAP	AF	US 2001/0043606	11-22-2001	Han et al.

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	Cite No.	Foreign Document Identification	PUBL. DATE (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	TRANSLATION	
					YES	NO
BAP	AG	EP 1052814 A	11-15-2000	Nortel Networks		X
↓	AH	WO 03/085909	10-16-2003	Cisco Technology		X

Examiner Signature	<i>Bob A. Phunkulh</i>	Date Considered	1/9/08
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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE MODIFIED PATENT AND TRADEMARK OFFICE				Application Number	10/822,908
				Filing Date	April 12, 2004
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				Group Art Unit	2661
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Sheet	2	of	2	Attorney Docket No.	00240

PENDING U.S. PATENT APPLICATIONS				
EXAMINER'S INITIALS	Cite No.	Application Number	Filing Date (MM-DD-YYYY)	Name of Applicant
BAP	BA	10/109,785	03-30-2002	Bonomi et al.
↓	BB	10/339,032	01-09-2003	Cohen et al.
↓	BC	10/684,282	10-10-2003	Patrick A. Costello

EXAMINER'S INITIALS	Cite No.	OTHER DOCUMENTS
BAP	BD	ANDERSON ET AL., <i>High-Speed Switch Scheduling for Local Area Networks</i> , ACM Transactions on Computer Systems, Vol. 11, No. 4, November 1993, pp 319-352.
↑	BE	MCKEOWN ET AL., <i>Achieving 100% Throughput in an Input-Queued Switch</i> , in IEEE Infocom, 1996.
	BF	NICK MCKEOWN, <i>The iSlip Scheduling Algorithm for Input-Queued Switches</i> , IEEE/ACM TRANSACTIONS ON NETWORKING, Vol. 7, No. 2, April 1999, pp. 188-201.
	BG	MCKEOWN ET AL., <i>Tiny Tera: A Packet Switch Core</i> , IEEE MICRO, Vol. 17, Issue 1, Jan.-Feb. 1997, pp. 26-33.
	BH	NICK MCKEOWN AND THOMAS E. ANDERSON, <i>A Quantitative Comparison of Scheduling Algorithms for Input-Queued Switches</i> , COMPUTER NETWORKS AND ISDN SYSTEMS, Vol. 30, No. 24, pp 2309-2326, December 1998.
	BI	PANKAJ GUPTA AND NICK MCKEOWN, <i>Designing and Implementing a Fast Crossbar Scheduler</i> , IEEE MICRO, Vol. 19, Issue 1, Jan.-Feb. 1999, pp. 20-28.
↓	BJ	WENZHE CUI ET AL., <i>A threshold based scheduling algorithm for input queue switch</i> , IEEE, 2001, pp. 207-212.
↓	BK	CHAO H J ET AL., <i>Centralized contention resolution schemes for a large-capacity optical ATM switch</i> , ATM WORKSHOP PROCEEDINGS, 1998 IEEE, Fairfax, VA, USA pp. 11-16, May 26, 1998.

Examiner Signature	<i>Bob A. Dunn</i>	Date Considered	1/4/06
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